

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

**Title of
Invention**

SYSTEM AND METHOD FOR PROTECTING AGAINST SHORT
CIRCUITS IN ELECTRIC POWER DISTRIBUTION
ARCHITECTURES WITH TWO VOLTAGE LEVELS

Application Number :

Confirmation Number:

First Named Applicant: Carles Borrego Bel

Attorney Docket Number: 8153ES

Art Unit:

Examiner:

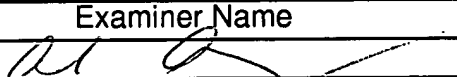
Search string: (6232674 or 6151222 or 5418401).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
AA	1	6232674	2001-05-15	Frey et al	A	307	10.1
M	2	6151222	2000-11-21	Barrett	A	363	16
AA	3	5418401	1995-05-23	Kaneyuki	A	307	10.1

Signature

Examiner Name	Date
	3/21/06